



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: SOUK, Jun-Hyung; LEE, Jeong-Young; YOON, Jong-Soo; CHOI, Kwon-Young; and BAEK, Bum-Ki

Assignee: Samsung Electronics Co., Ltd.

Title: THIN FILM TRANSISTOR ARRAY PANEL AND MANUFACTURING METHOD THEREOF

Application No.: 10/759,389 Filing Date: January 16, 2004

Examiner: Ton, Minh Toan T. Group Art Unit: 2871

Docket No.: AB-1351 US Confirmation No.: 6707

San Jose, California
April 26, 2006

Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PETITION FOR EXTENSION OF TIME

Dear Sir:

Applicants respectfully petitions for a one-month extension of time within which to file the Response to the Final Office Action mailed on December 29, 2005 thus allowing the undersigned until April 29, 2006 to file a Response.

Please charge the amount of \$120.00 set forth in the enclosed transmittal letter.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on April 26, 2006.

10/26/2006 4/26/2006
Attorney for Applicant(s) Date of Signature

Respectfully submitted,

10/26/2006
Hugh H. Matsubayashi
Attorney for Applicants
Reg. No. 43,779

LAW OFFICES OF
MacPherson, Kwok, Chen &
Heid LLP
1762 Technology Drive
Suite 226
San Jose, CA 95110
Telephone (408) 392-9520
Fax (408) 392-9262

05/03/2006 AKELECH1 00000004 502257 10759389

01 FC:1251 120.00 DA